The Department Newsletter

Department of Electronics and Telecommunication Engineering

DR. YOGESH S. ANGAL
HOD, E&TC

Content :-

HOD MESSAGE

Dr. Yogesh S. Angal
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HOD (E&TC) JSPM BSIOTR Wagholi

E&TC Graduates find wide opportunities in today's growing communications era. Our mission is to cultivate creativity and innovativeness in teaching learning process for developing competent professionals also to equip graduating students with skill sets to meet current and future demands of industry, academia and society through well designed curriculum The teaching module comprises lectures, tutorials and practical's. The methodology adopted in the classroom teaching is based on application of innovative strategies, comprehensive lectures, regular assignments and interaction between teachers and students.

NEWSLETTER TEAM

Prof.Meenakshi Annamalai
Chief Editor

Kalyani Pawase
SE, E&TC

Snehal Satav
SE, E&TC

Vision Statement of E&TC Department :-
To emerge as a department of repute compatible to the changing needs of society at large.

Mission Statement of E&TC Department :-
M1: To cultivate creativity and innovativeness in teaching learning process for developing competent professional.
M2: To equip graduating students with skill sets to meet current and future demands of industry, academia and society through well designed curriculum.

PROGRAM EDUCATIONAL OBJECTIVE(PEO's) of E&TC Department :-
PEO-1 :- Ability to pursue professional carrier.
PEO-2 :- An ability to adopt rapid changes in core & allied domain of Electronics and Telecommunication Engineering fulfilling industrial & societal needs.
PEO-3 :- An ability to deal with societal problems ethically delivering sustainable solution.

PROGRAM SPECIFIC OUTCOMES (PSO's) of E&TC Department :-
PSO1 (Fundamental Knowledge and Analysis) :- An ability to understand, apply and analyze the engineering problems relevant to Electronics & Telecommunication Engineering.
PSO2 (System Design) :- Design the Electronics & Telecommunication Engineering systems using latest hardware and software tools by applying significant analytical knowledge.
PSO3 (Societal and Environmental Issues) :- An understanding societal, environmental, health, safety and cultural issues with ethical responsibility and function effectively as an individual and heterogeneous group.

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Vision Statement of BSIOTR :-
To provide, nurture and maintain an environment of high academic excellence, research and entrepreneurship for all aspiring students, which will prepare them to face global challenges maintaining high ethical and moral standards.

Mission Statement of BSIOTR :-
Satisfy the aspirations of youth force, who want to lead nation towards prosperity through techno-economic development.

APEX FORUM ON "RECENT TRENDS IN ARTIFICIAL INTELLIGENCE"

The Institution of Electronics & Telecommunication Engineers (IETE), Pune Centre in association with Department of Electronics and Telecommunication, JSPM's Bhivarabai Sawant Institute of Technology & Research, Wagholi, Pune had organized an Apex Forum on "Recent Trends in Artificial Intelligence" (RTAI-2k18) on28/02/2018. Theme of the Apex Forum: The theme of this Apex Forum is to discuss on overall research goal of artificial intelligence to create technology that allows computers and machines to function in an intelligent manner Participants should get acquainted with machine mimics "cognitive" functions that humans associate with other human minds, such as "learning" and "problem solving". Dr. A. A. Ghatol was Invited as Chief Guest of the program, Prof. S.K.Khedkar, mentor West Zone IETE, New Delhi was invited as guest honour. Mr. G. K. Dawara, Charman IETE Pune Centre and Mr. K. S. Shende, Secretary, IETE Pune Centre, Dr, T.K.Nagaraj, Principal, JSPM's BSIOTR, Dr. D.D.Shah, Dr. P. H. Patil, Dr. V.V.Kayande, CampusbDirector JSPM Wagholi Campus were on the dais. Dr. A.A. Ghatol told that Artificial Intelligence (AI) plays vital role in development of India. He motivated the participants to develop some products of AI useful for society. Prof. S.K. Khedkar given simple examples of Artificial Intelligence and its applicability in our daily life.

QUICK CIRCUIT MAKING

Round I: Multiple Choice Questions 1. 25 Number of objective questions are to be solved within 30 min. 2. Objective questions will be based on subjects like Basic Electronics, Basic Electrical, Electronic Devices and Circuits (EDC), Digital Logic Design (DLD), Linear Integrated Circuits (LIC). 3. Use of Scientific Calculator is allowed. 4. Make a Tick Mark ($\sqrt{\ }$) on appropriate choice 5. Once tick mark is done on the choice it cannot be altered. For multiple tick marks, zero marks will be given. 6. If book or any other supporting material is found, that group will be disqualified from thecompetition. 7. Result of Round-I will be displayed on Notice Board. 8. After evaluation selected groups will appear for second round.
 Round II: Questions on Circuit Debugging 1. 10 Number of questions are to be solved within 15 min. 2. Maximum errors to be found for each electronic circuit. 3. Result of Round II will be displayed on Notice Board. 4. After evaluation selected groups will appear for third round. Round III: Circuit design and implementation 1. Problem statements will be given for circuit design and implementation to each group or team. 2. On completion, the final circuit has to work for all the test cases. 3. On completion of first circuit implementation, Group may implement another circuit within prescribed time (30mminutes). Number of circuits implemented will also be the criteria for selection.